

INNOVATION IN DER PCB-TECHNOLOGIE

Waldkirch, 25.10.2017

Dirk Gennermann, Head of Product Marketing

The logo for FED, featuring the letters F, E, and D in a dark teal color. The letter E is stylized with a light grey triangle pointing upwards behind it, and the letter D has a light grey triangle pointing downwards behind it.The logo for SCHWEIZER, featuring a stylized blue and green circular icon on the left and the word SCHWEIZER in blue capital letters on the right.

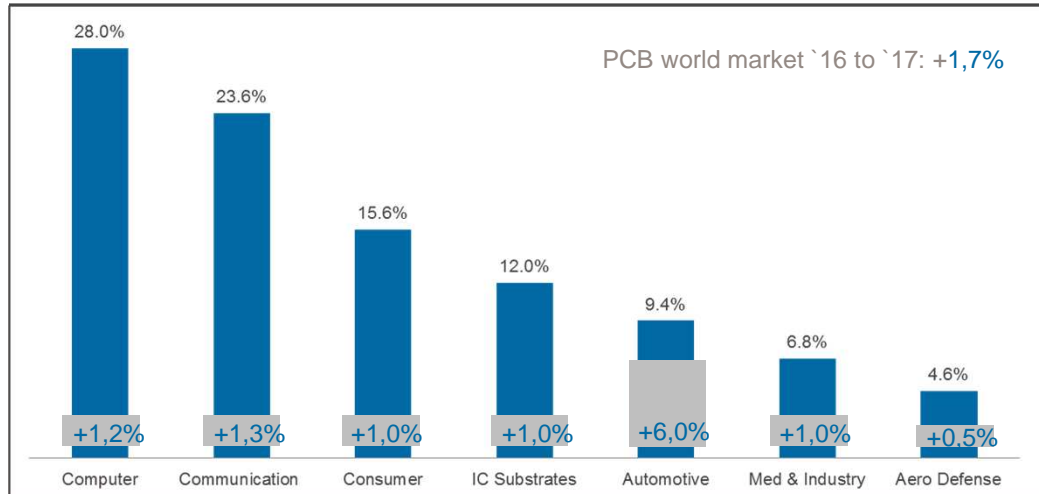
AGENDA

1. PCB Market overview
2. Schweizer @ a glance
3. Funding projects at Schweizer
4. Power PCB Trends
5. Power PCB Trends with Embedding

The PCB Market: 58.3 bn USD in 2016

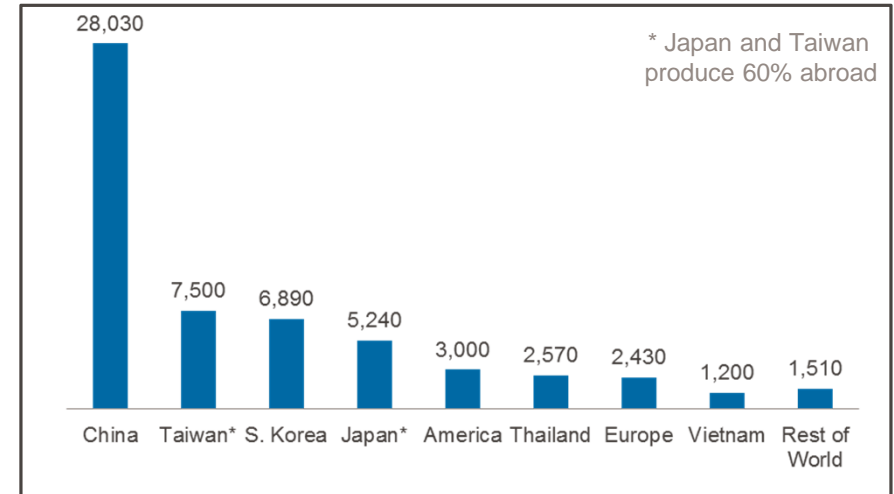


PCB Market by Industries



Source NTI

World PCB Output in 2016 in mio. USD by country

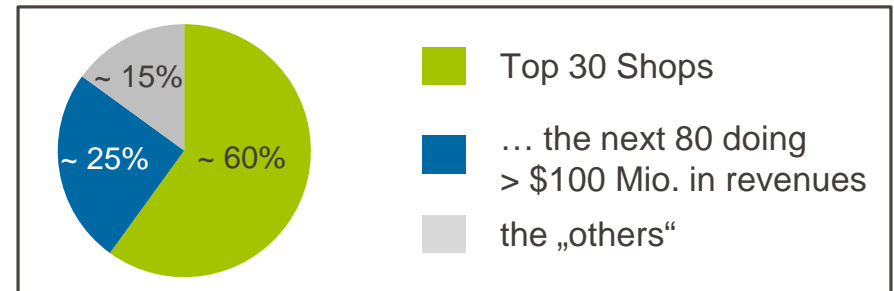


Source NTI

Different kinds of rigid PCB FABs

Lower Layer Count	Higher Layer Count	HDI	Super fine Pitch

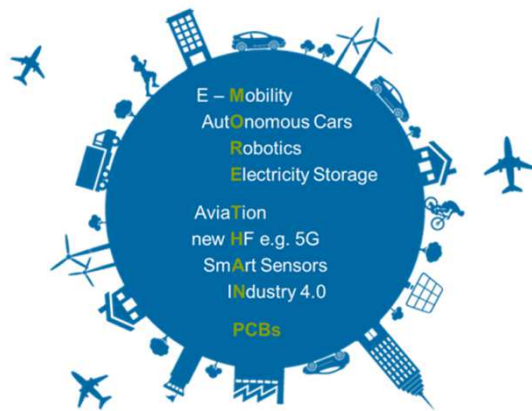
2,400 PCB Shops



Source NTI

SCHWEIZER @ A GLANCE

- 168 years successful company history
- 116.1 Mio. € revenue in 2016
- 5th growth year in a row
- Automotive 76%, Industrial 18%, Other 6%
- >780 employees
- Partners: WUS, Infineon, Meiko, Elekonta
- A leading supplier for Sensor and Power PCB solutions
- ... with numerous substantial novel product concepts for tomorrows:



Schweizer and its global Partners



Chip Embedding



HF PCBs / HiLo PCBs



Technology development



Reliable mass production



Quick turnaround samples



AUTOMOTIVE INDUSTRY WILL BE DISRUPTED



Tony Seba, Disruption Guru

“The Automotive Industry will be disrupted on **battery cells, sensors and social networks.**”

IN **2030**





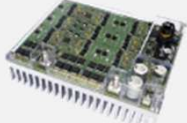


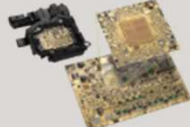

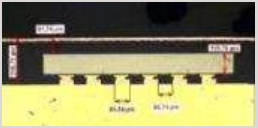
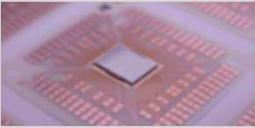
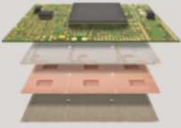
ALL CARS

WILL BE

- **ELECTRIC**
- **AUTONOMOUS**
- **SHARED**

SCHWEIZER TODAY SENSOR- & POWER PCB SOLUTIONS



	Sensor PCB Solutions	Power PCB Solutions
	Autonomous Driving	CO₂ Reduction
Application Examples	<p>Radar Sensors  Source: Bosch</p> <p>Cameras  Source: Continental</p>	<p>Optimisations of conv. Drivetrain  Source: Continental</p> <p>LED Headlamps  Source: Daimler</p> <p>Hybrid & E-Cars Drive – DC/DC – AC/DC  Source: Infineon</p>
PCB Solutions	<p>77GHz Hybrid  Source: Bosch</p> <p>FR4 Flex  Source: Continental</p>	<p>“Bond PCBs”  Source: Continental</p> <p>Power PCBs  e.g.: IMS Board e.g.: Inlay Board</p>
Embedding Solutions	<p>Due to further increasing performance- and miniaturisation needs, many applications will use Embedding Solutions in the Future</p>	
	<p>μ^2 Pack® </p>	<p>i^2 Board® </p>
		<p>p^2 Pack® </p>

FUNDING PROJECTS AT SCHWEIZER

STRENGTHEN OUR CORE COMPETENCIES



Project VoLiFa 2020


- Embedding of Power and Logic Semic.
- Highest Power Dissipation




Smart PVI-Box

Smart PVI-Box

- High Temperature/ High Thermal Conductivity
- Embedding of Wide-Band-Gap Semiconductors



BMBF-Project KoRRund

- Embedding of Radar ICs
- Novel Radar Antenna Technologies




HIGH FREQUENCY CURRENT TEMPERATURE AND MINIATURISATION

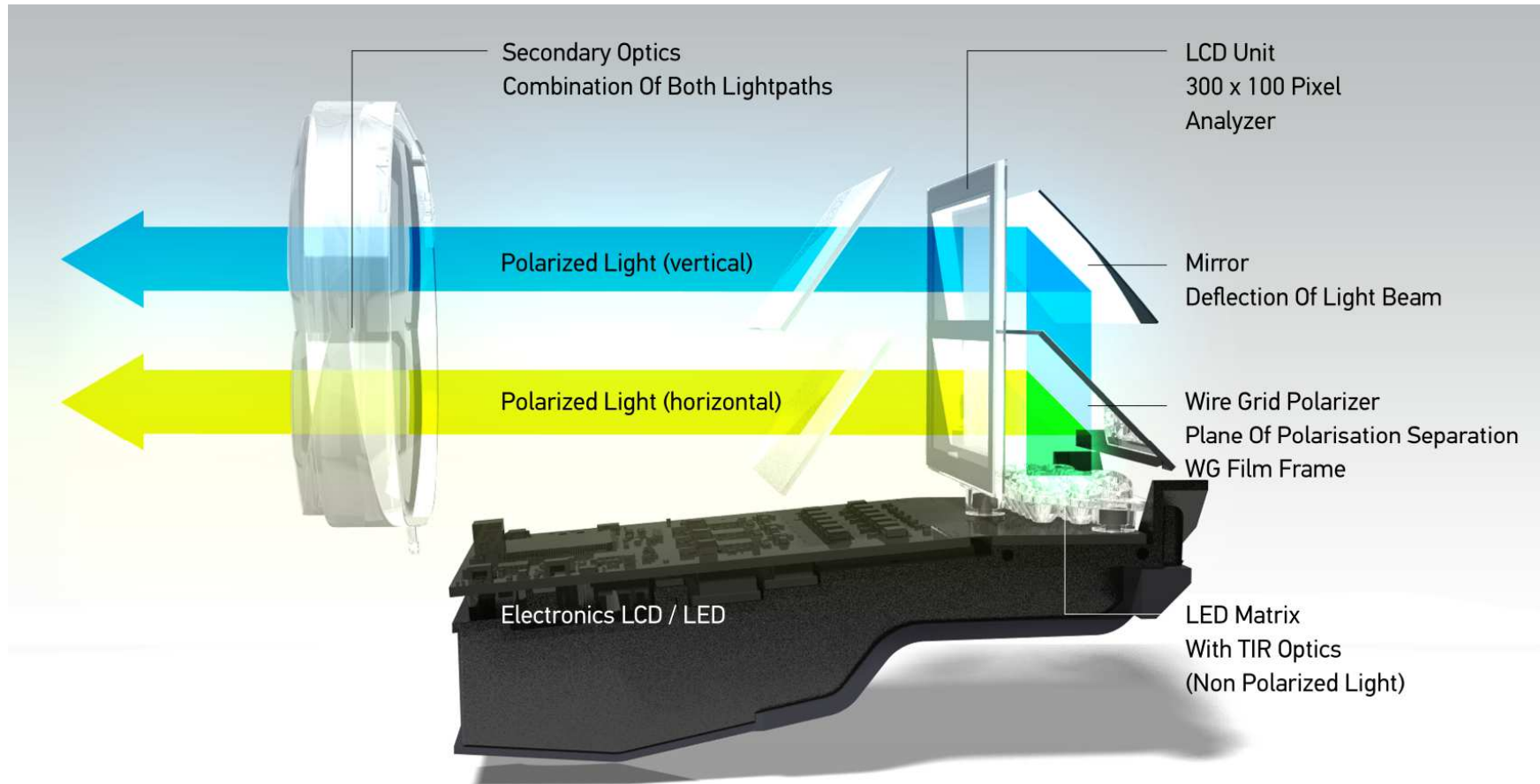
THE ART OF LIGHT



VoLiFa 2020 Head Lamp



Liquid Crystal HD head lamp

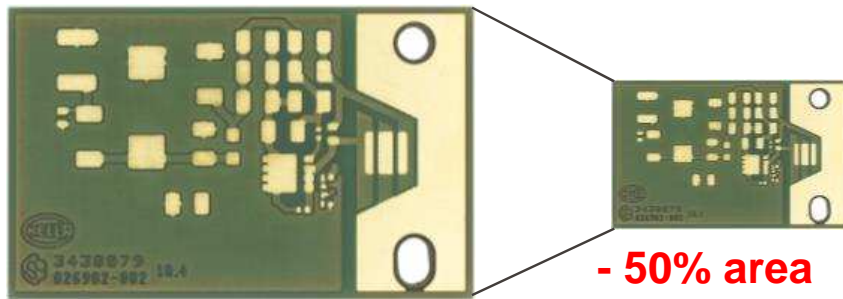


source: Hella

PCB FOR FOREFIELD MODULE

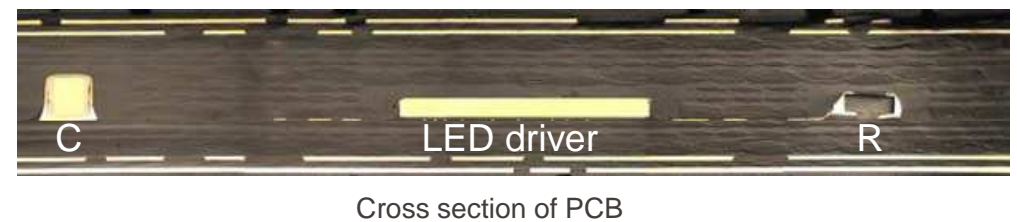
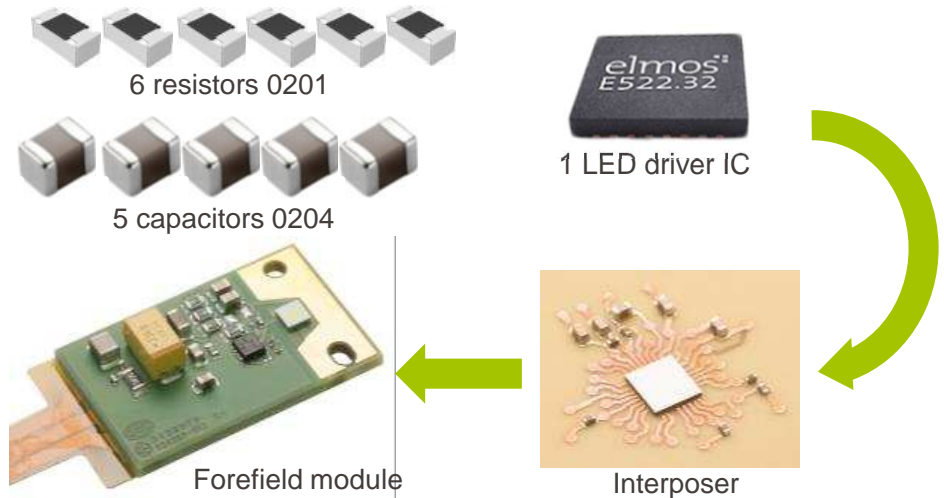
Innovation task:

- miniaturization
- More functions in less volume
- „More than Moore“



solution:

- Embedding of twelve components into PCB

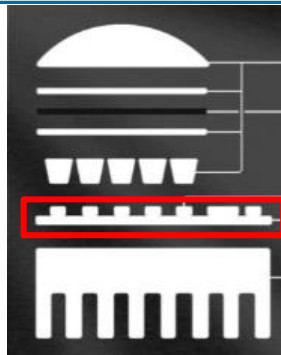


STRENGTHEN OUR CORE COMPETENCIES



Project VoLiFa 2020

- Embedding of Power and Logic Semic.
- Highest Power Dissipation



Smart PVI-Box

Smart PVI-Box

- High Temperature/ High Thermal Conductivity
- Embedding of Wide-Band-Gap Semiconductors



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HIGH FREQUENCY CURRENT TEMPERATURE AND MINIATURISATION

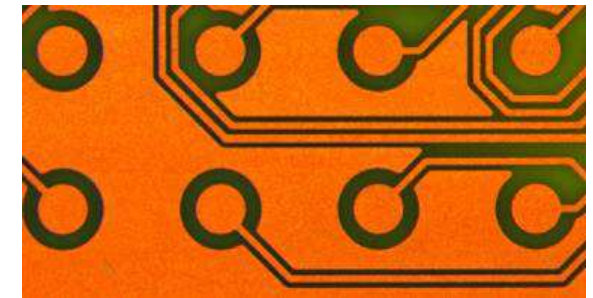
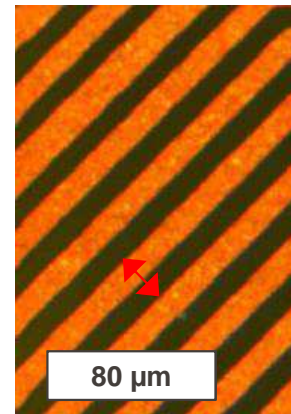
Impact on radar antennas

- Spec limit of traces today: undercut $<20\ \mu\text{m}$
- New Technology: „no“ undercut ($\sim 2\ \mu\text{m}$)
- Sharp antenna edges



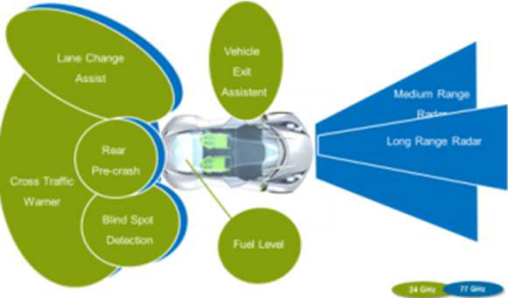
Benefits for Layout

- $40\ \mu\text{m}$ lines/spaces on HF side
- Via in pad without dimple



In development

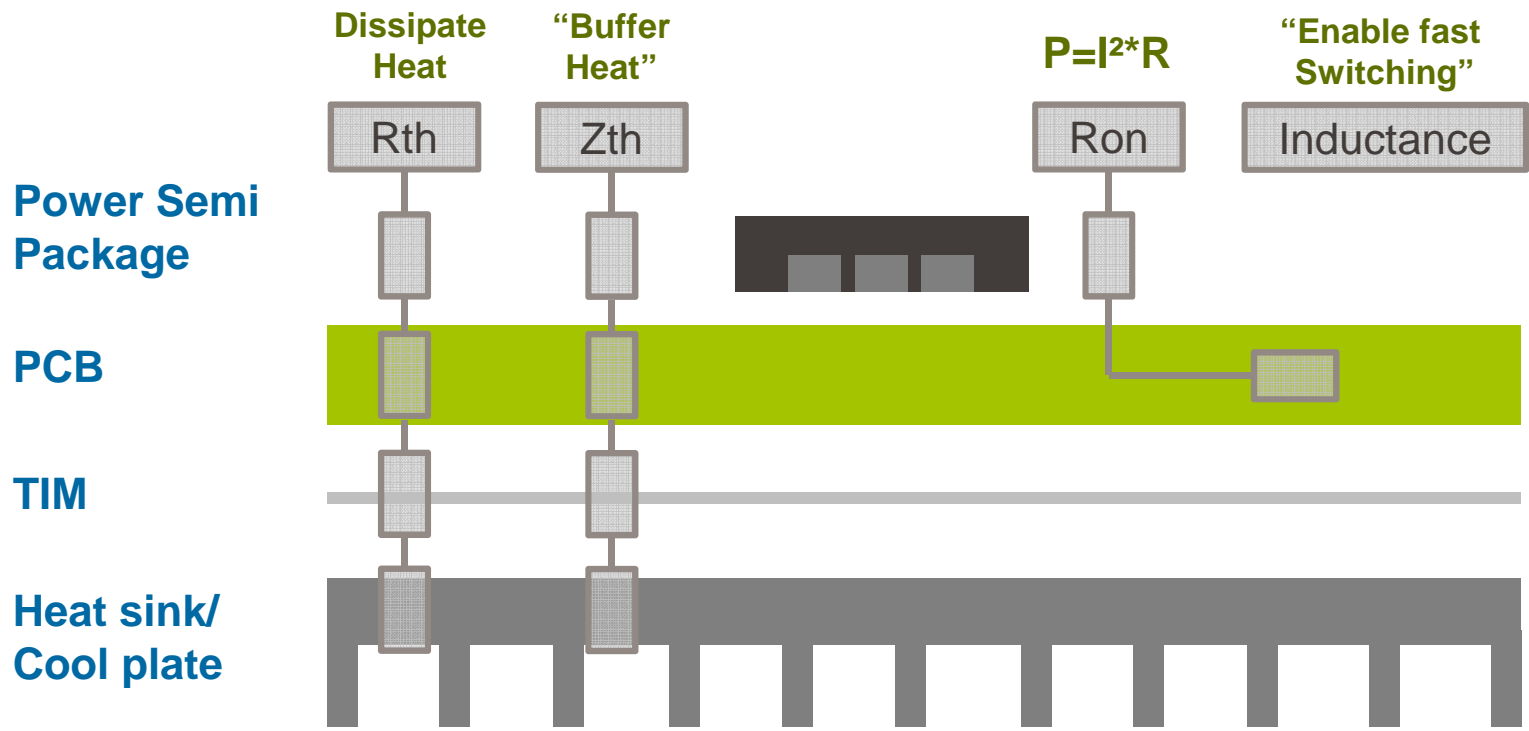
EMBEDDING THE NEXT BIG TREND FOR HF



Heat Dissipation	-	+
Shielding	-	+
Duty Cycle	-	+
Reliability	-	+
Form factor	-	+
Testability	+	-
PCB Cost	+	-

POWER PCB TRENDS

ELECTRICAL AND THERMAL PERFORMANCE OF A POWER DESIGN



POTENTIAL POWER OPTIONS



Option 1

Logic & Power
PCB



Option 2

Logic PCB



Power PCB
Heavy Cu, Inlay, IMS



Option 3

Logic PCB



Ceramic
 Al_2O_3 , AlN, Si_3N_4



NEW
Option 4

Logic PCB



p² Pack
(Embedding)



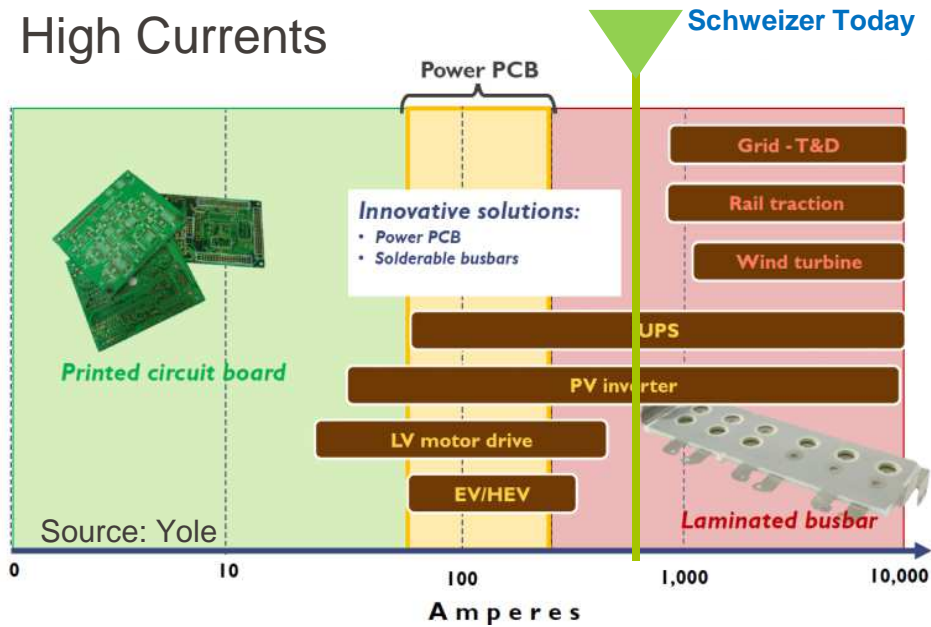
NEW
Option 5

Smart p² Pack
(Embedding)

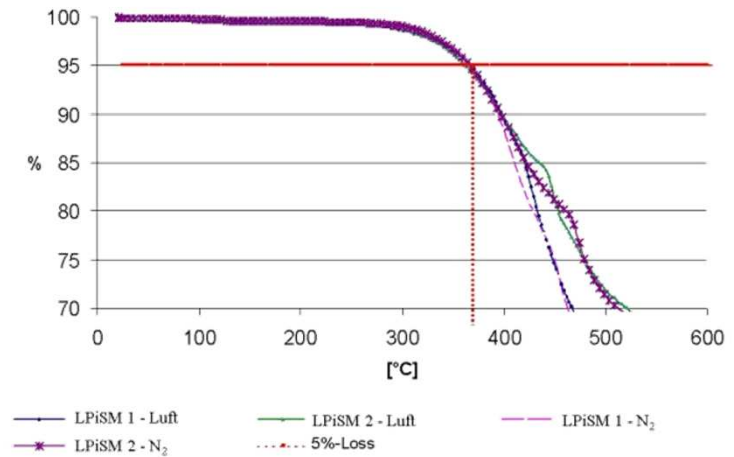


POWER PCBs

High Currents



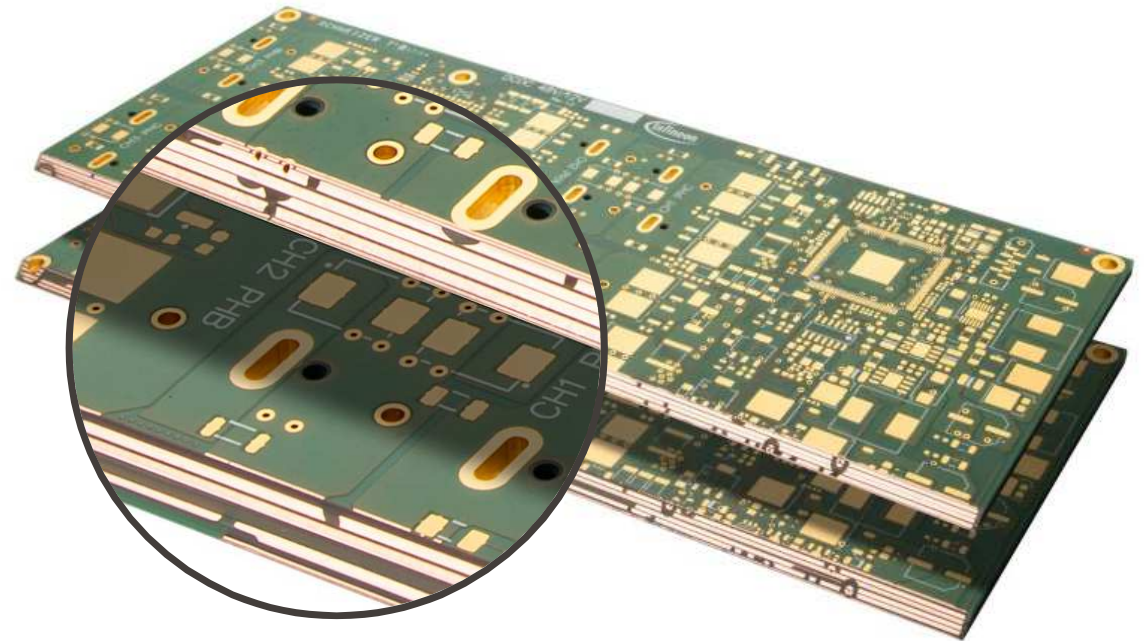
High Temperature




High Voltage



48/12V DC/DC DEMO



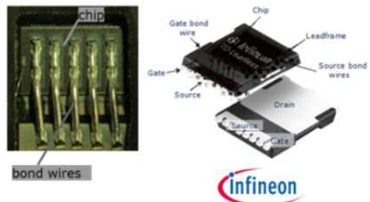
- in cooperation w/ 
- 4 x 400 μm Cu &
4 x 70 μm Cu

- w/ Heavy Copper $T^2 = 2,6 \text{ mm}$
- w/ Heavy Copper = 3,5 mm

ON-RESISTANCE LOSSES OF PCB MATTERS

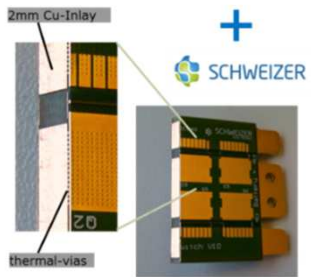
High current system approach, combining

- High current MOSFET technology **84 μ Ohm @ 25°C**
- High current Inlay Board technology **29 μ Ohm @ 25°C**



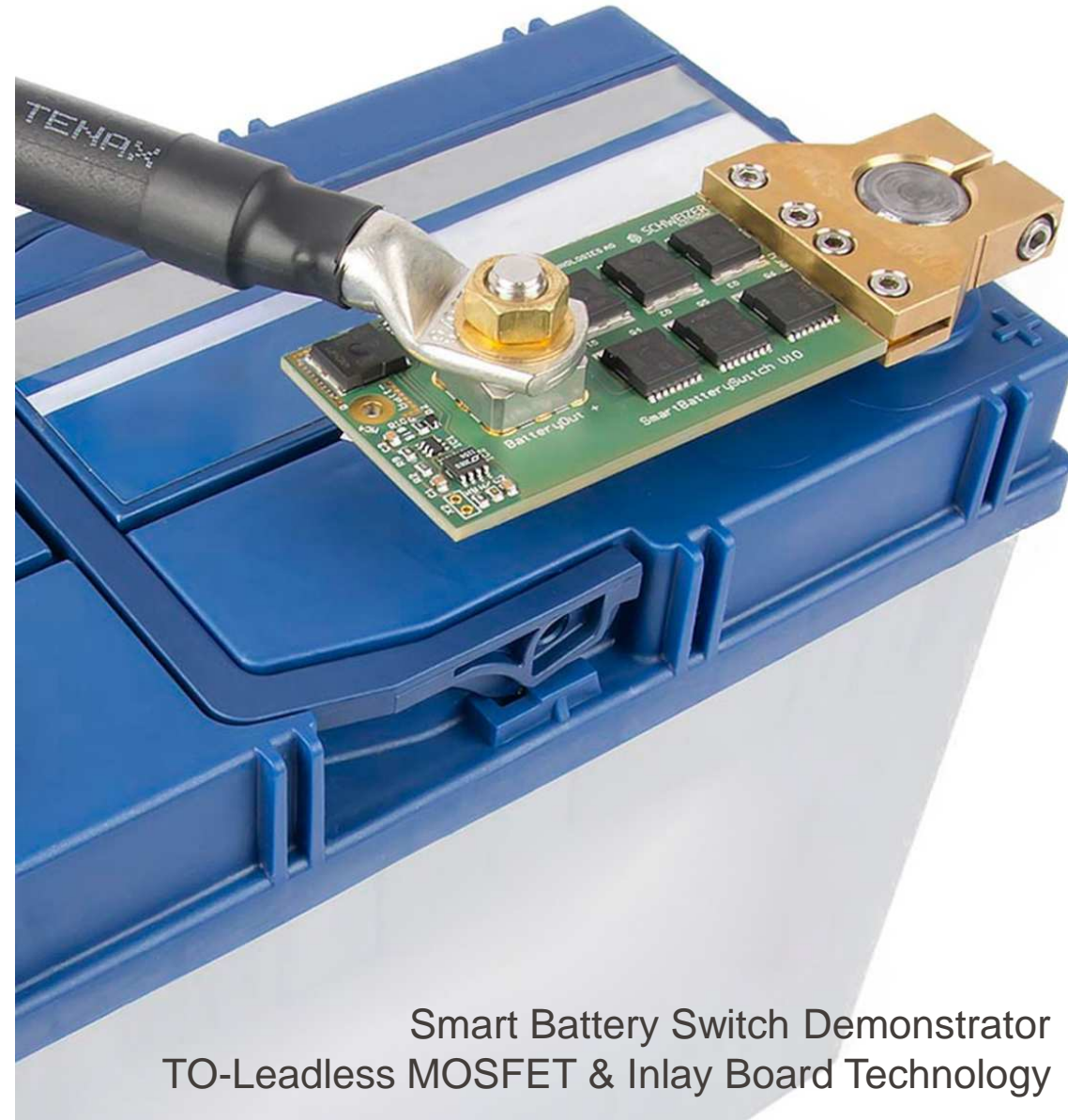
1. MOSFET losses $P=I^2 \cdot R$

P @ 300 A:
@ 25°C: = 7,6 W (75%)
@ 150°C: = 15,12W (79%)



2. PCB losses (2mm Inlay)

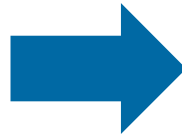
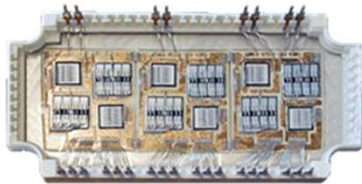
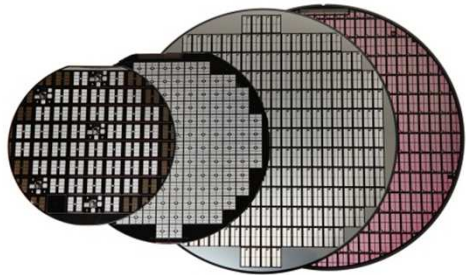
P @ 300 A:
@ 25°C: = 2,6 W (25%)
@ 150°C: = 4,05 W (21%)



Smart Battery Switch Demonstrator
TO-Leadless MOSFET & Inlay Board Technology

POWER PCB TRENDS WITH EMBEDDING

EMBEDDING – WHAT’S THAT?

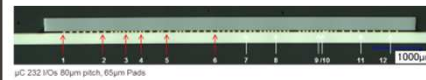


System in Package

System in PCB

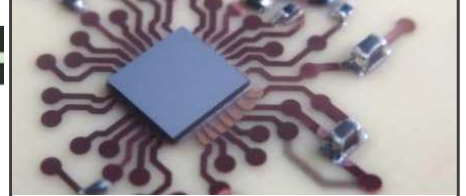
Logik

e.g. μ^2 Pack[®]



PLP Solutions

e.g. i^2 Board[®]

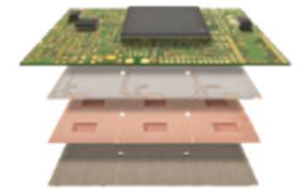


Power

e.g. p^2 Pack[®] DSV



e.g. p^2 Pack[®]



Embedding – requires a new business model & supply chain

POWER ON DEMAND WITH SMART p² PACK® IN PARTNERSHIP WITH INFINEON



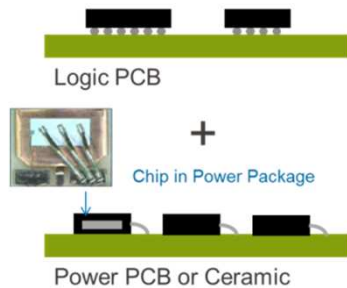
#1 in Power Semiconductors



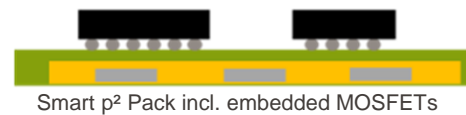
Leading Supplier of Power PCBs

... join forces to establish a new Semiconductor & PCB solution

Today



Tomorrow



Advantages:

- Miniaturization
- Performance increase
- System cost reduction

Application Example



SMART p² PACK GOAL

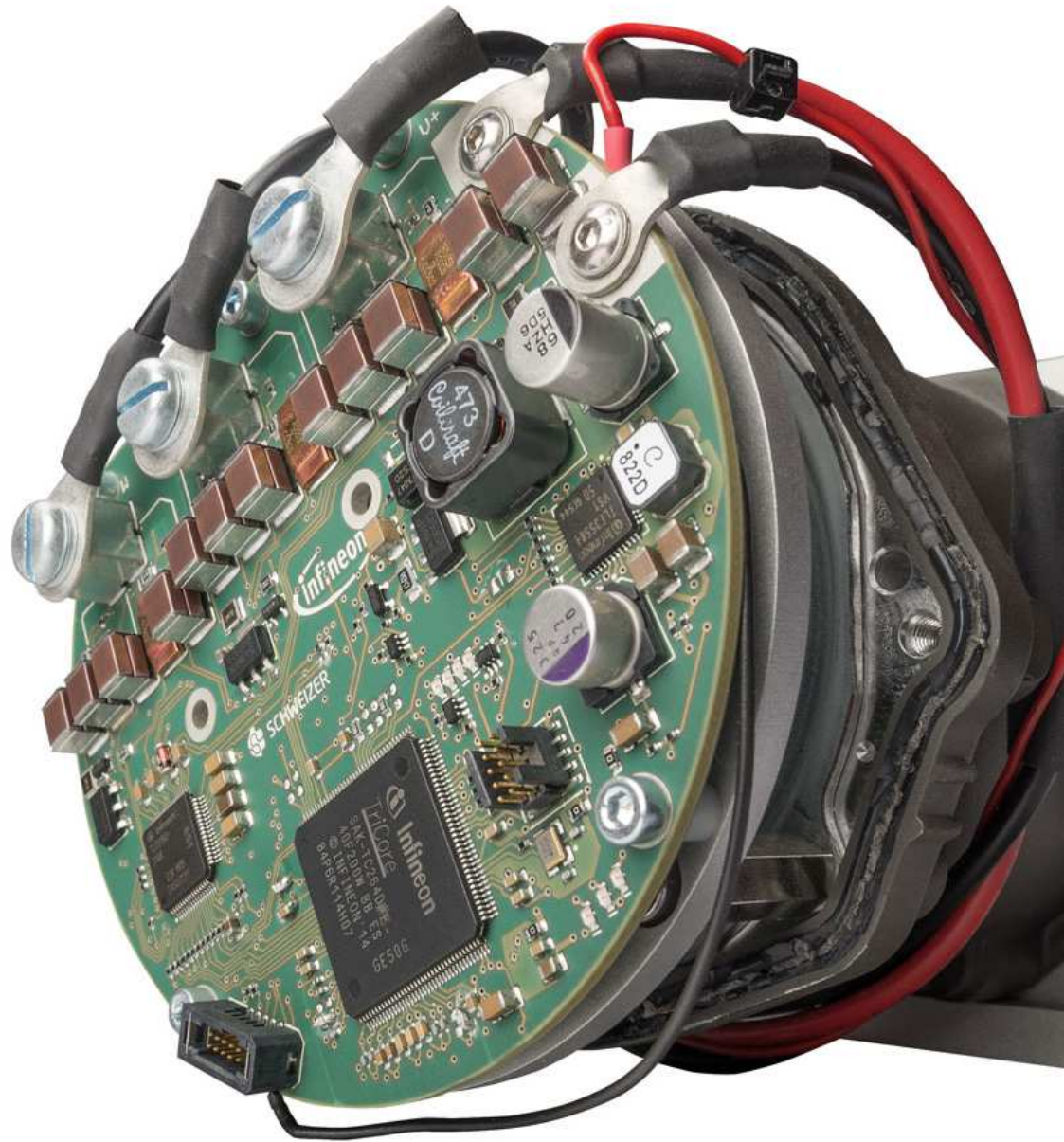
p² Pack® offers

either **~30%* less power dissipation**

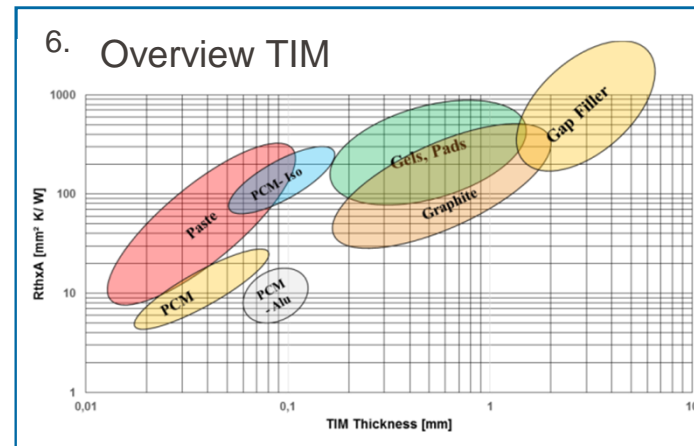
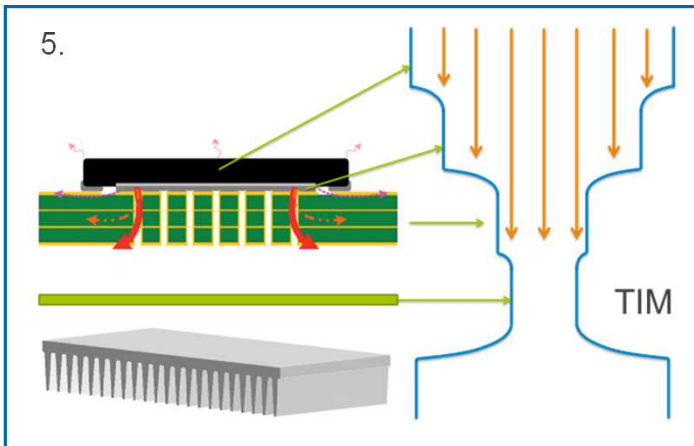
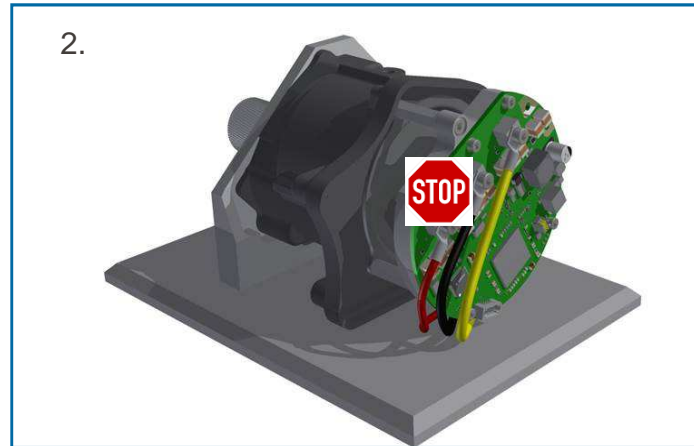
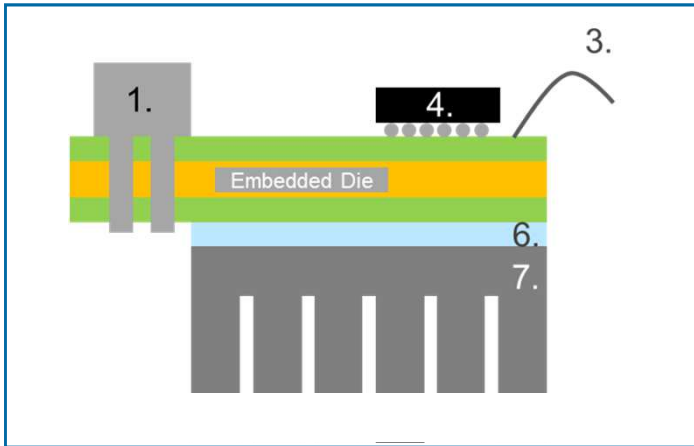
or **~50%* higher performance**

compared to a conventional packaged MOSFET
(w/ same Die Size) on PCB solution.

* Engineering estimate: exact % may vary depending on the
specific application



EMBEDDING REQUIRES NEW SYSTEM CONSIDERATIONS



1. How to bring high currents "ON and OFF the PCB"?
2. How to connect Motor and Inverter?
3. How to connect the Logic- and the Power PCB?
4. Component Placement?
5. Optimize the heat flow
6. Performance TIM (and other new BAMA for High Tg and Voltage), Thickness, Isolation?
7. Water-, Air – no Cooling?

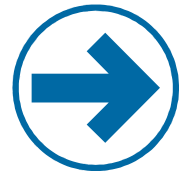
p² PACK DEMONSTRATORS



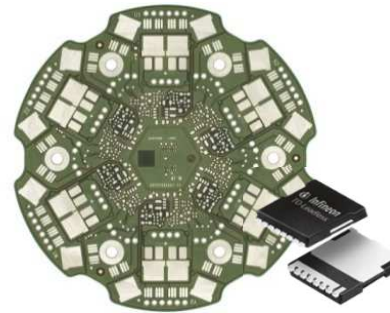
48V Belt-driven Starter Generator

Solution Today

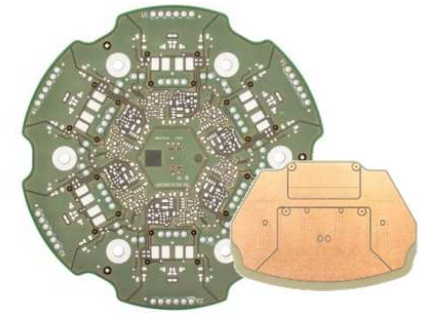
Source: Conti



BSG Demonstrator in cooperation w/ Continental and Infineon



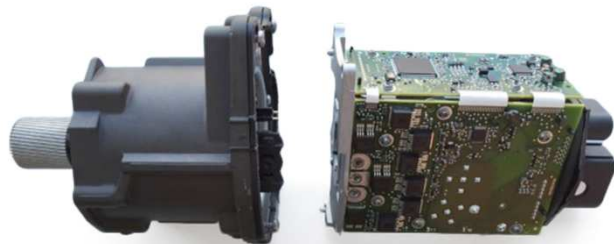
Inlay Board + Logic PCB



Smart p² Pack

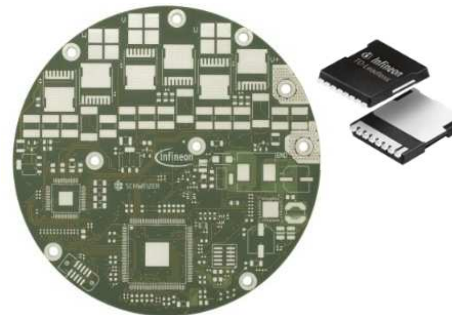
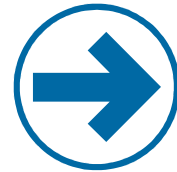
p² Pack with embedded MOSFETs

Auxiliary Drive Demonstrator in cooperation w/ Infineon

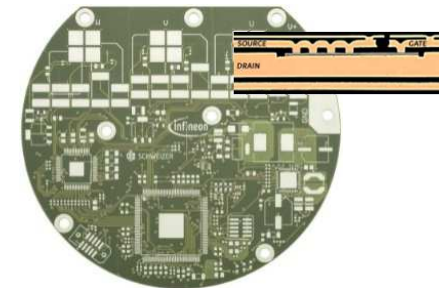


Solution Today

Source: VW



Heay Copper Solution



Smart p² Pack

p² Pack with embedded MOSFETs

COMPARISON OF “TOLL PACKAGE” W/ p² PACK DATASHEET LEVEL



Embedding Solution

Target Data Sheet

IAUE303N10S5N011

OptiMOS™-5 Power-Transistor

Product Summary

V_{DS}	100	V
$R_{DS(on),max}$	1.1	mΩ
Die Size	30.25	mm ²
Die Thickness	105	μm

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal characteristics¹⁾						
Thermal resistance, junction - Back side	R_{thjB}	-	-	-	0.3	K/W
Drain-source on-state resistance ¹⁾	$R_{DS(on)}$	$V_{GS}=6V, I_D=75A$	-	-	1.5	mΩ
		$V_{GS}=10V, I_D=100A$	-	-	1.1	

Package Solution

Preliminary Data Sheet

IAUT300N10S5N015

OptiMOS™-5 Power-Transistor

Product Summary

V_{DS}	100	V
$R_{DS(on)}$	1.5	mΩ
I_b	300	A

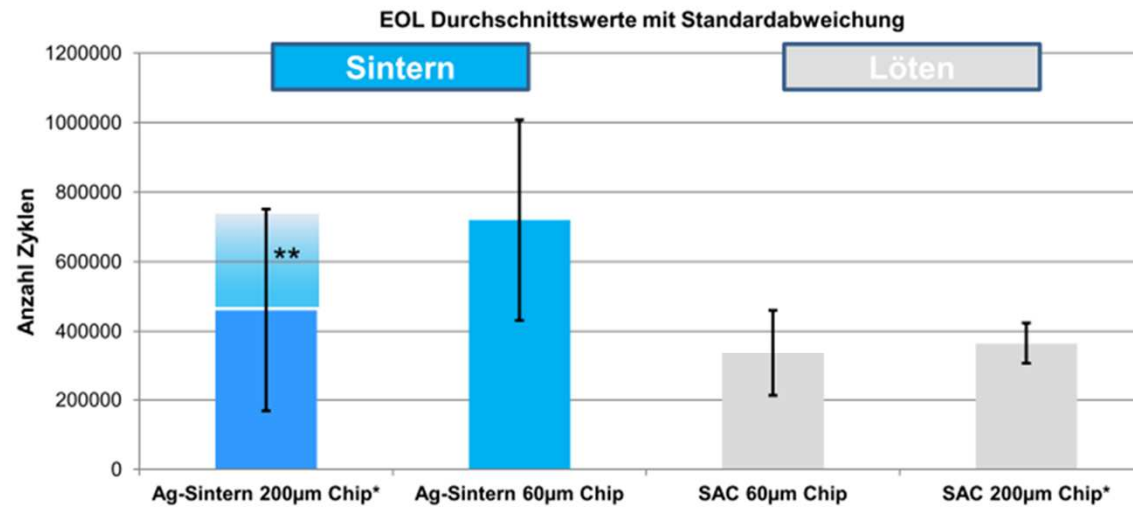
Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal characteristics²⁾						
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=6V, I_D=75A$	-	1.6	2.0	mΩ
		$V_{GS}=10V, I_D=100A$	-	1.3	1.5	

➔ 1,1 mOhm * 36% ➔ 1,5 mOhm

CHIP EMBEDDING OFFERS POTENTIAL TO FURTHER IMPROVE RELIABILITY



Powercycling p² Pack



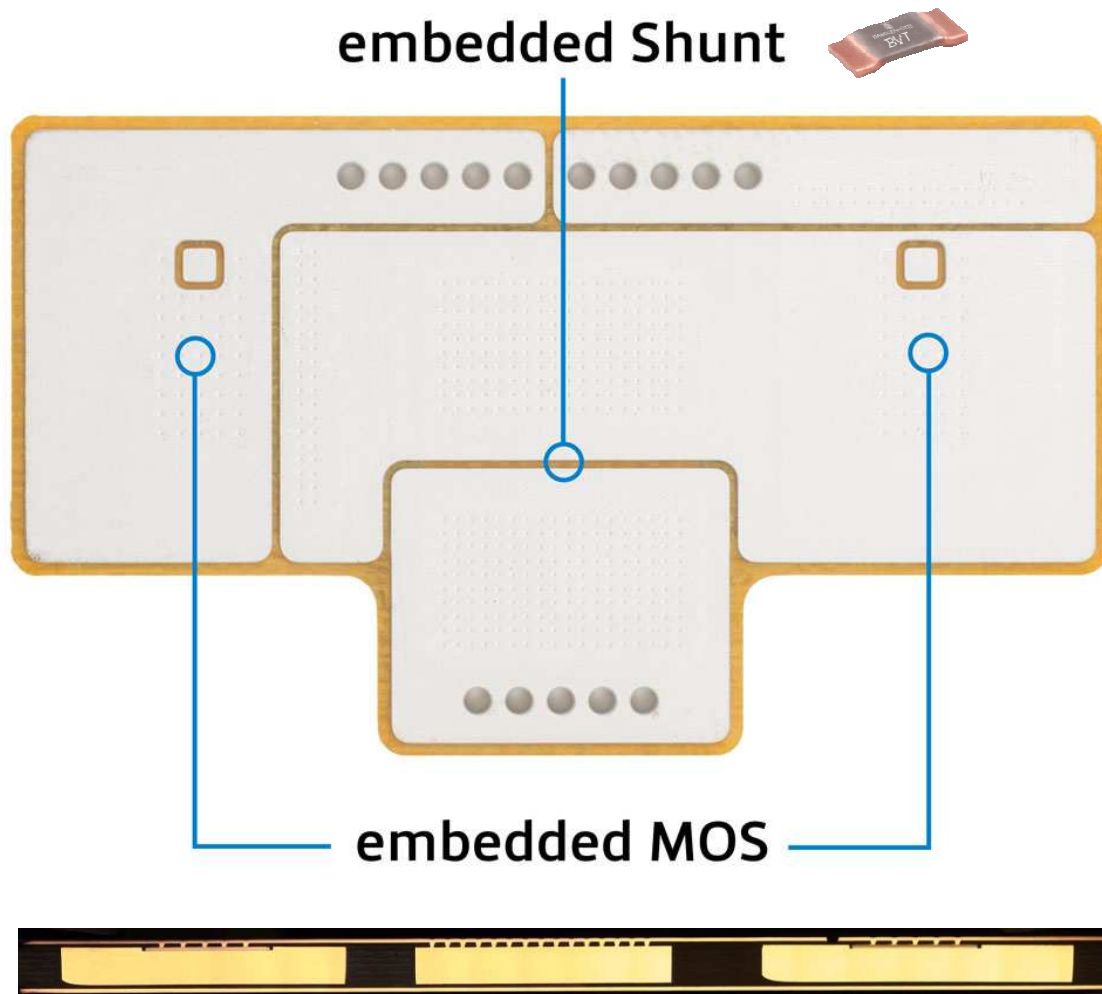
*Zyklisierung mit 80K Hub gestartet, dann auf 120K gesteigert. Umrechnung 80K->120K mit angenommenen Coffin-Manson-Koeffizient=3

** 4/5 der gesinterten 200µm Chip Muster haben bei Testabbruch EOL nicht erreicht. Schattierter Bereich symbolisiert qualitativ höheres Lebensdauerpotential.

- Chip: MOSFET, 60V
- LP-Material: Panasonic R14-T
- Testbedingungen: 120K Temperaturhub, 2s/2s Pulslänge
- EOL-Kriterien: el. Ausfall; Temperaturdrift >30K; R_{th} +20%

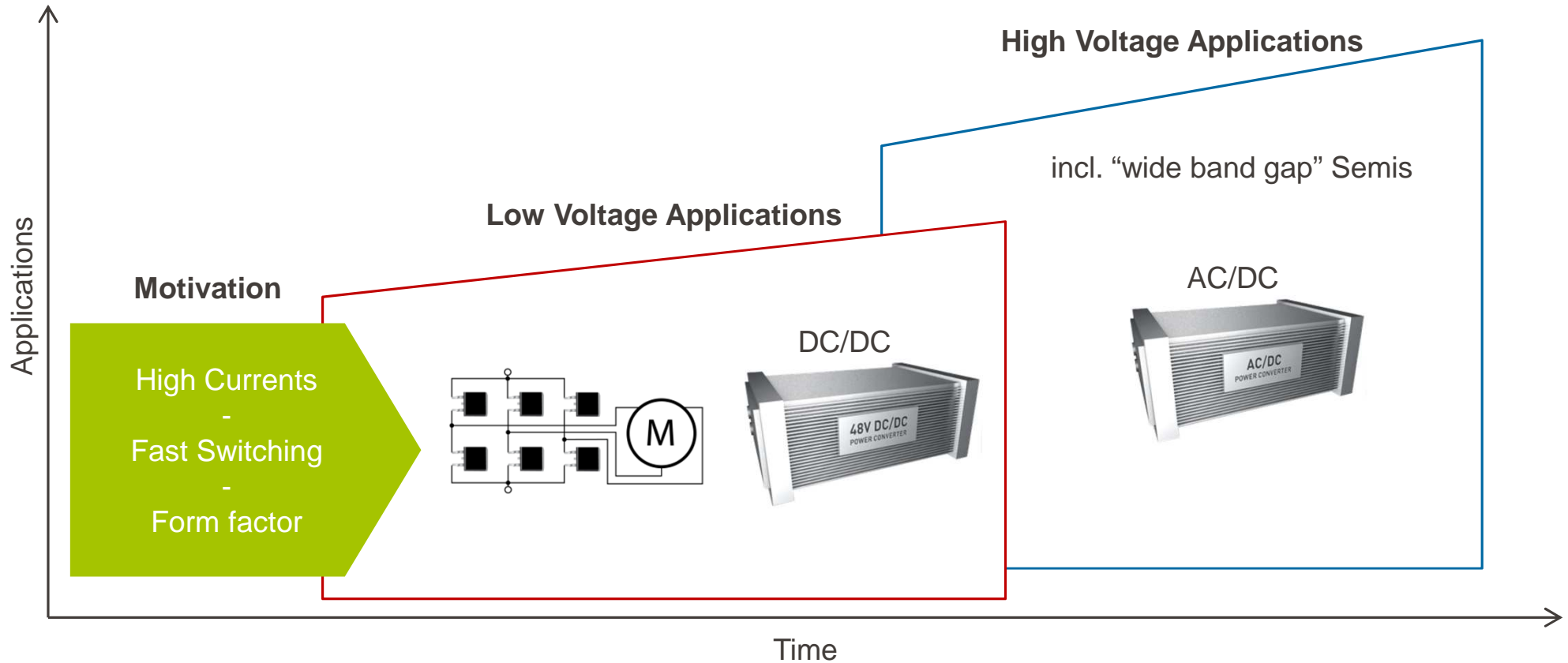
Silbergesinterte p² Pack Muster erreichten gegenüber den gelöteten Varianten eine deutlich höhere Beständigkeit beim Powercycling

p² PACK HALF BRIDGE W/ EMBEDDED SHUNT



- Embedded Shunt: 0,05 to 0,1 mOhm.
- 0 - 300 A
- 0,5 - 30 mV
- Thermal Dissipation 4,5 - 9 W
- Temperature rise 3 - 5 K
- Contact resistance < 1%(Microvias) of nominal resistance value

p² PACK INDUSTRIALIZATION ROADMAP



POWER ON DEMAND – POTENTIAL SMART p² PACK APPLICATIONS



	Electrification ~ 500W – 5kW	Downsizing ~ 3kW – 50kW	Hybrid & E-Drive ~ >50kW
HV			<p>Hybrid (+ DC/DC)</p> <p>E-Car (+ AC/DC)</p>
48V	<p>Active Roll Stabilize <small>(Source: Silver Atena)</small></p> <p>Climate Compressor</p>	<p>48/12V DC/DC</p> <p>Crankshaft Starter Generator</p>	<p>Mild Hybrid</p>
12V (+ Optional 2 nd 14V)	<p>Gearbox</p> <p>Power Steering</p> <p>E-Pumps + Fans + Heaters</p>	<p>E-Compressor</p> <p>Belt Driven Starter Generator</p>	

SPHIN(X) 100 KW DEMO



Highly integrated electric axle drive for passenger cars

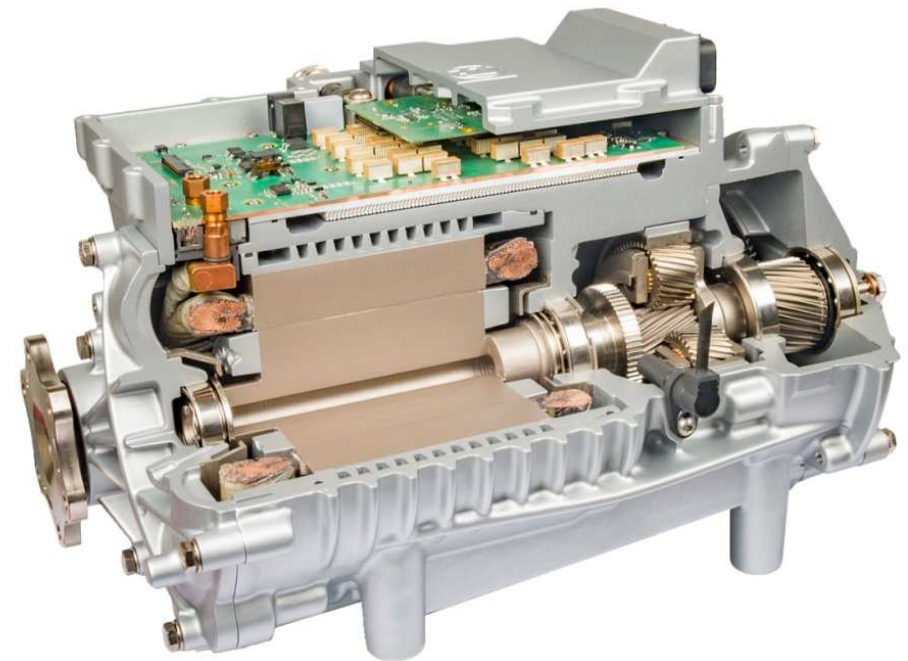
Joint research project (01.01.2013 – 31.12.2016)

- ZF Friedrichshafen AG (project coordination)
- Daimler AG
- Infineon Technologies AG
- TLK Thermo GmbH
- Institut für Thermodynamik / Technische Universität Braunschweig

Power-PCB by Schweizer; inverter in p² Pack technology

Compact, highly integrated, scalable inverter

- $U_{\text{nenn}} = 300 \text{ VDC}$
- $I_{\text{max}} = 450 \text{ A}_{\text{rms}}$
- $P_{\text{max}} = 100 \text{ kW}$




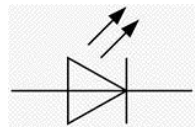


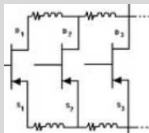


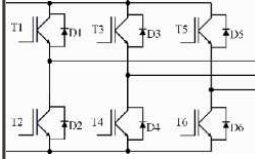

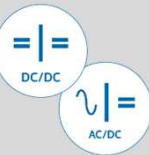
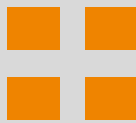
DAIMLER



Gefördert durch:
 Bundesministerium
für Wirtschaft
und Energie
aufgrund eines Beschlusses
des Deutschen Bundestages

APPLICATION FIT

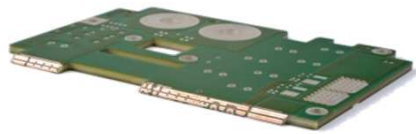


Major Applications	Block Diagram	Schematic View	Heavy Copper	Inlay	Cu IMS	p ² Pack
LED 				X	X	
Switch 				X		
Motor Drive 			X	X	X	X
DC/DC, AC/DC 	H-Bridge DC/DC		X	X	X	X

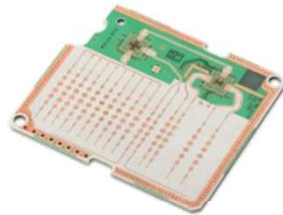
MORE THAN PCBS



New Functionality

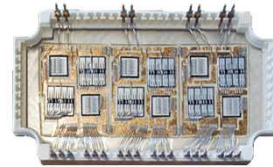


e.g. Power Boards



e.g. Radar Antennas

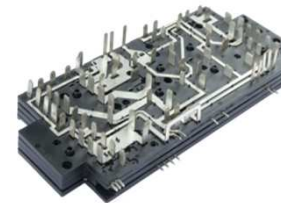
Cost reduction



Ceramics
(DCB/ LTCC) –

Connector/ Cable -

& Lead frame
Replacement



Miniaturization



Flex



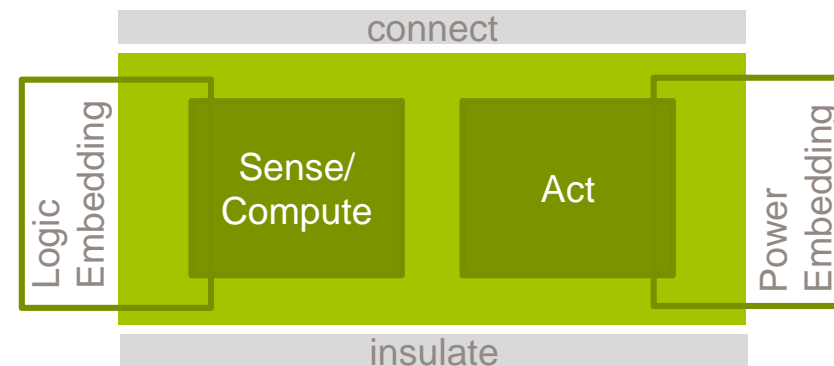
HDI



Active- and Passive
Component Embedding



example





more
than PCBs